

REMARKS

Claims 1-38 are currently active.

The Examiner has rejected Claims 1, 16, 26-28, 33-35 and 38 as being anticipated by Ganmukhi. Applicant respectfully traverses this rejection.

The Examiner has taken a portion of the architecture taught by Ganmukhi and attempted to suggest that the second level of hierarchy scheduler 80 receives connections corresponding to the groups of connections associated with the first level generator. However, a clear review of the teachings of Ganmukhi shows that the scheduler 80 schedules between the first scheduler output 32, the schedulers' outputs 42, 52 and 62, and the scheduler output 72. All of these outputs form one single group. To reiterate, they form a group, where group is in the singular.

The claim limitation refers to groups, where groups is the plural. The second level generator as defined by the Examiner using the architecture taught by Ganmukhi, shows that the static priority scheduler 90 receives groups but not from the first level generator, which the Examiner defines as scheduler 80. The second level generator in terms of the elements of Ganmukhi receives the output from the scheduler 20 which is not part of the first

level generator (specified by the Examiner) and a single output from the scheduler 80 formed from the single group of outputs 32, 42, 52, 62 and 72. Thus, the interpretation of the elements taught by Ganmukhi in regard to the limitations of Claim 1 are not met because the second level generator is not associated with connections corresponding to the groups of connections associated with the first level generator. Claim 16 is also not anticipated for this reason by Ganmukhi.

Claims 26, 27 and 33 have been amended to include the limitations of Claim 2.

Claim 34 has the limitation of a scheduler having a schedule bitmap which is either statically stored or dynamically generated. Ganmukhi does not teach or suggest this limitation.

Claim 35 has the limitation of a scheduler which maintains active bit maps which indicate which connections are active. Ganmukhi does not teach or suggest this limitation.

The Examiner has rejected Claims 2-5 and 17-20 as being unpatentable over Ganmukhi in view of Lahat. Applicant respectfully traverses this rejection.

As explained above, the Examiner has applied interpretations to the elements of the architecture taught by Ganmukhi to try to arrive at applicant's claimed invention. In fact, the outputs the Examiner suggests form a group or groups are in fact individual outputs that only come from their respective scheduler. By definition, if the group was inactive, the scheduler would have nothing to send, so there be no reason to need an additional filter mechanism which filters out inactive groups of connections. Thus, it would not be obvious, nor would there be any reason why one skilled in the art would take the teachings of Ganmukhi and apply any type of filter mechanism as found in Claim 2.

Referring to Lahat, there is disclosed an asynchronous transfer mode switch utilizing optical wave division multiplexing. Lahat teaches wave division multiplexing technology enables the simultaneous transmission of multiple data channel connections on the same physical optical fiber. This is achieved by utilizing several different wavelengths of the same optical fiber at the same time. See column 3, lines 37-41 of Lahat. Lahat teaches an ATM switch which utilizes an all optical switching fabric to form switching functions. The switch is based on fiber optics and dense wavelength division multiplexing. See column 4, lines 16-23.

The Examiner cites Lahat for the simple reason that Ganmukhi does not teach a bit map generator for generating schedule bitmap indicating the group to be scheduled for

service. Lahat does not teach or suggest a scheduler having a first level generator or a second level generator is found in Claim 1 or a scheduler that is based on a schedule bit map. For the reason that Ganmukhi nor Lahat teach or suggest the limitations of applicant's claimed invention, a scheduler, Ganmukhi in view of Lahat does not teach or suggest any of the claims of applicants.

Moreover, it is clear the only reason that the Examiner is combining these references is through the use of hindsight from applicant's claimed invention. It is also not patent law to use hindsight from applicant's claims, nor to have applicant's claims serve as a road map to find the various elements of applicant's claims and different limitations, and having found the various elements and different limitations, conclude that applicant's claimed invention is patentable. Accordingly, the applied art of record, separately or in combination, does not anticipate nor make obvious any of applicant's claims.

Accordingly, Claims 2-5 and 17-20 are not obvious over the applied art of record.

The Examiner has rejected Claims 29, 32, 36 and 37 as being over Ganmukhi in view of Lahat. Applicant respectfully traverses this rejection.

Claim 29 is dependent to parent Claim 27, which has been amended. Claim 29 is patentable for the reasons Claim 27 is patentable.

Claim 32 is dependent on parent Claim 1 and is patentable for reasons Claim 1 is patentable.

Claim 36 has the limitation of a scheduler having a schedule bitmap and active bit maps which indicate which connections are active, the scheduler filters out inactive connections from the schedule bitmap by ANDing schedule bitmap with the active bit maps. The applied art of record does not teach or suggest this limitation.

Claim 37 has the limitation of a scheduler having schedule bit maps which can contain multiple bits per connection to schedule different types of bandwidth. The applied art of record does not teach or suggest this limitation.

In view of the foregoing amendments and remarks, it is respectfully requested
that the outstanding rejections and objections to this application be reconsidered and
withdrawn, and Claims 1-38, now in this application be allowed.

Respectfully submitted,

MEENARACHAGAN VISHNU

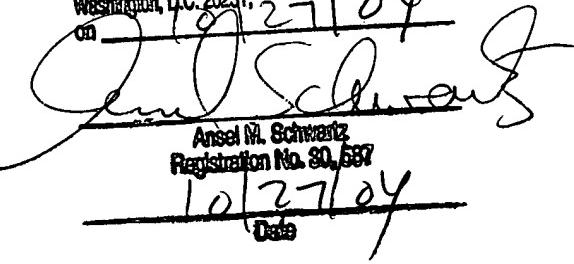
By 

Ansel M. Schwartz, Esquire
Reg. No. 30,587
One Sterling Plaza
201 N. Craig Street
Suite 304
Pittsburgh, PA 15213
(412) 621-9222

Attorney for Applicant

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Ansel M. Schwartz
Registration No. 30,587

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